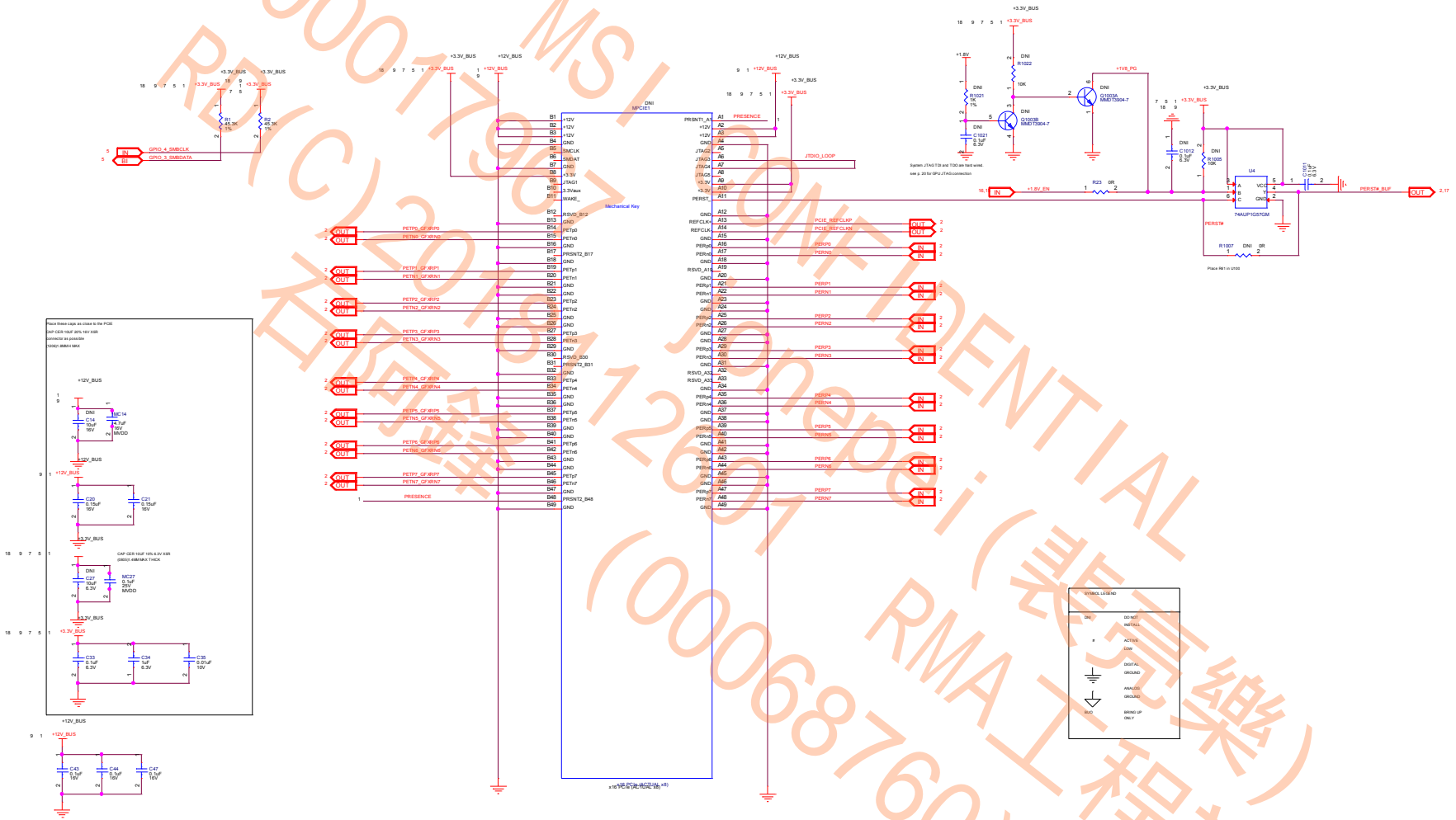


# PCI-EXPRESS EDGE CONNECTOR

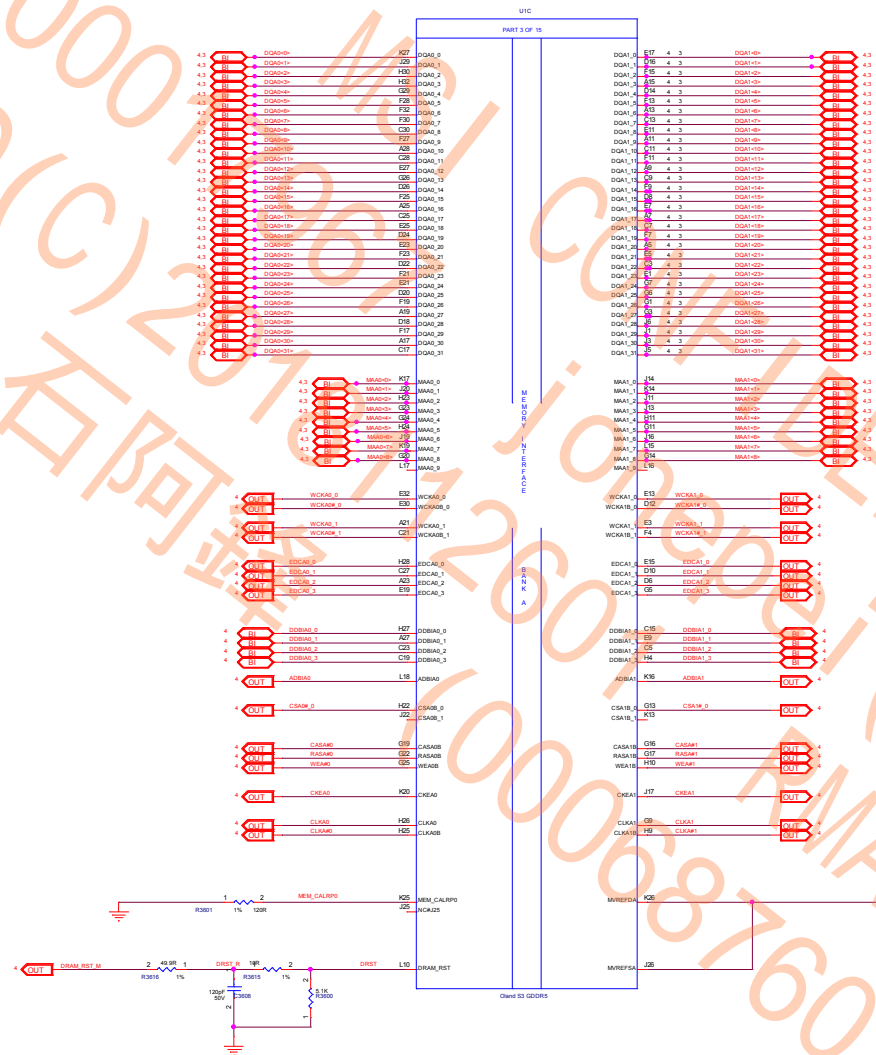



<b>AMD - PLATFORM HARDWARE ENG</b> #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203		CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC. © 2018 Advanced Micro Devices This AMD based schematic and design is the exclusive property of AMD, and is intended for internal use only. It is not to be distributed, copied, or otherwise used for any purpose other than the specific purpose for which it was created. AMD makes no representation or warranty of any kind regarding the accuracy or design, including, but not limited to, any product or component, or the results of any use of the information contained herein.	
SHEET:	PCE Edge Connector	REV:	P00
DATE:	Wed Aug 29 03:27:33 2018	SHEET NUMBER:	1 OF 20
DOCUMENT NUMBER:	105_d12900_00A	TITLE:	XXX

2

2

2



AMD - PLATFORM HARDWARE ENG		
#46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203		
SHEET: VDDCI		
DATE: Wed Aug 29 03:24:49 2018	REV: P00	
SHEET NUMBER: 3 OF 20		
DOCUMENT NUMBER: 105_412900_00A		
NOTES: NOTE		

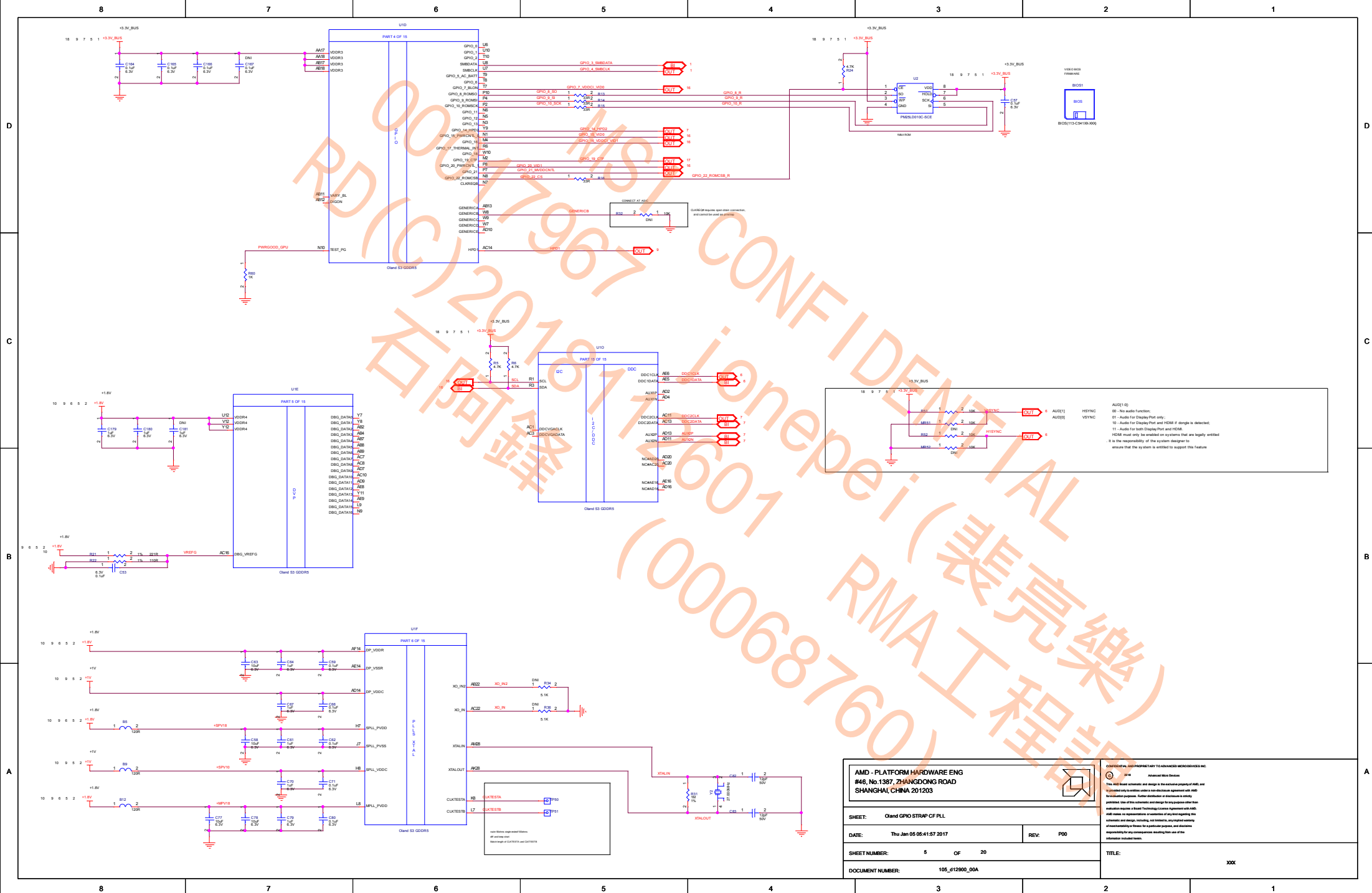
CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.

**© 2018 Advanced Micro Devices**

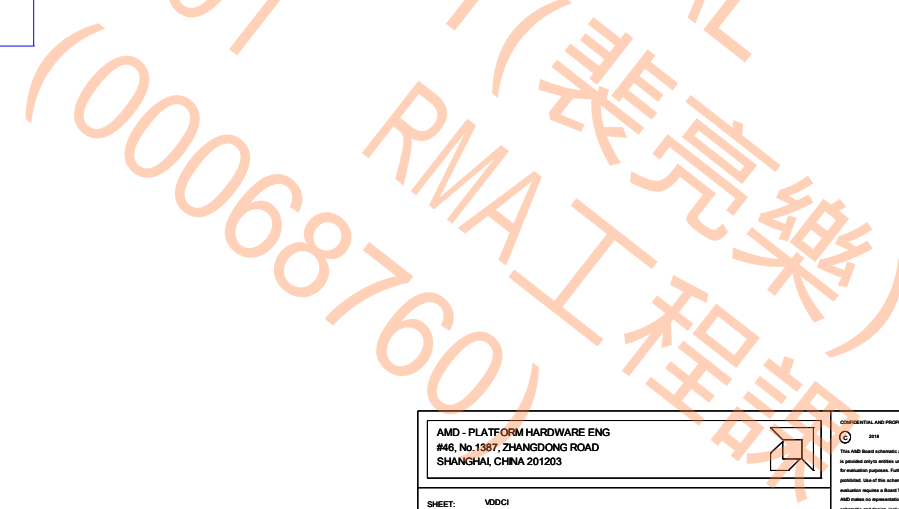
This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or dissemination is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.


TITLE: \_\_\_\_\_





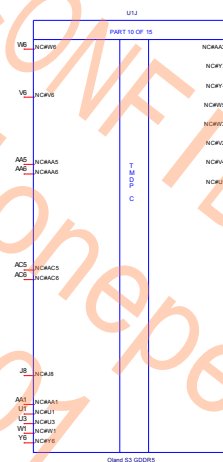
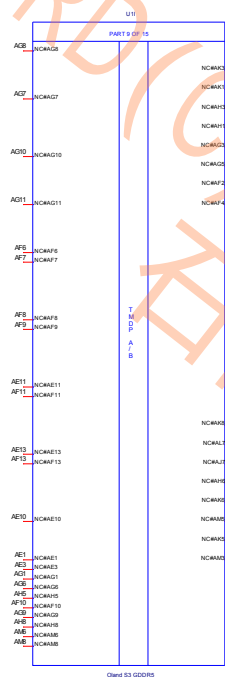




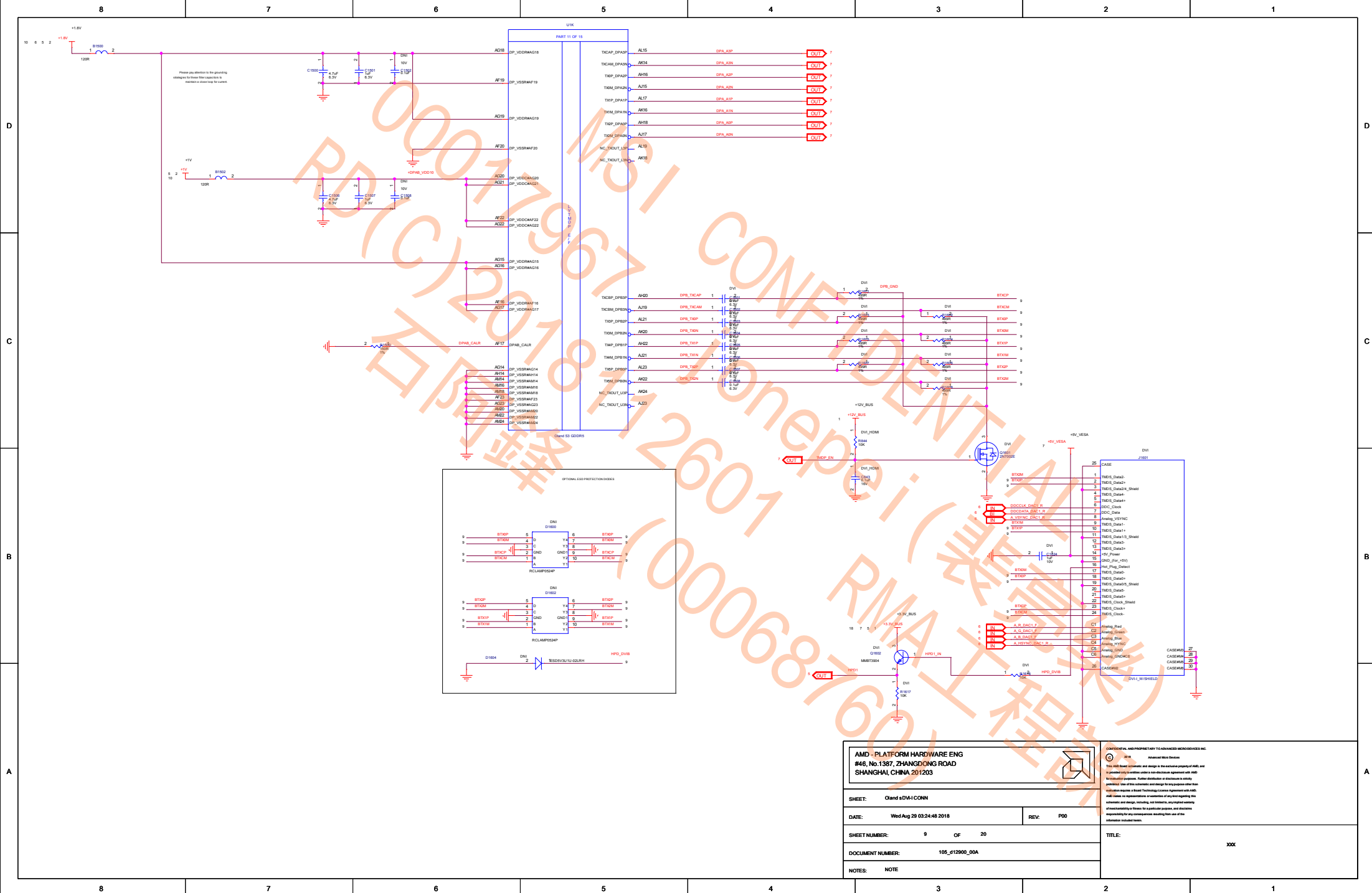
	<p><b>CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.</b></p> <p>© 2018 Advanced Micro Devices</p> <p>This AMD Brand schematic and design is the exclusive property of AMD, and is provided only for review within a non-disclosure agreement (NDA) for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of the schematic and design for any purpose other than evaluation requires a formal Technology Transfer Agreement signed with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, but not limited to, any implied warranty of non-infringement or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information disclosed herein.</p>	<p>000</p>
	<p><b>TITLE:</b></p>	<p>XXX</p>



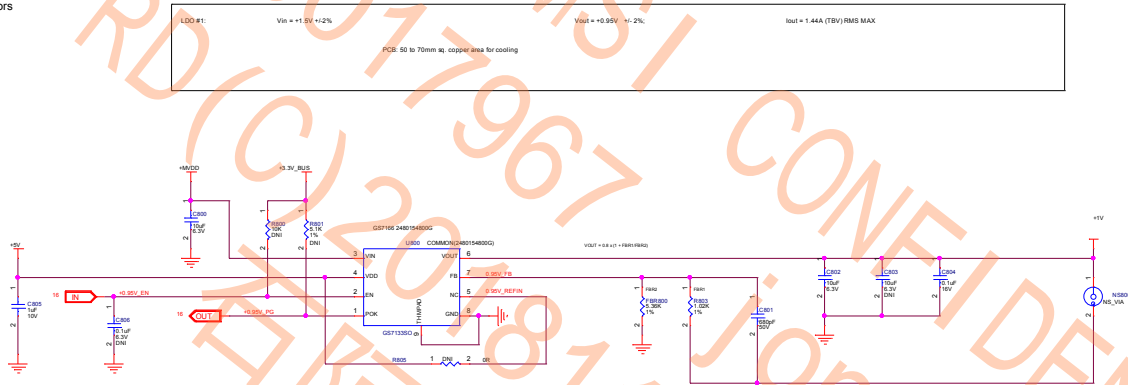




<p><b>AMD - PLATFORM HARDWARE ENG</b>  <b>#46, No.1387, ZHANGDONG ROAD</b>  <b>SHANGHAI CHINA 201203</b></p>	<div style="border: 1px solid black; width: 100px; height: 100px; margin: 0 auto; position: relative;"> </div> <p><small>CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC.        © 2012      Advanced Micro Devices</small></p> <p><small>1. AMD, its design, structure and design is the exclusive property of AMD, and is protected only in accordance with a non-disclosure agreement with AMD and its authorized programs. Further distribution or disclosure is strictly prohibited. Use of this information and design for any purposes other than those authorized requires a valid Technology License Agreement with AMD. AMD makes no representation or warranties of any kind regarding this information and design, including, but not limited, any implied warranty of non-infringement or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.</small></p>
<p><b>SHEET:</b>      Grand Unused Modules</p>	<p><b>FILE:</b>      XXX</p>
<p><b>DATE:</b>      Wed Aug 29 03:24:48 2018</p>	
<p><b>REV:</b>      P00</p>	
<p><b>SHEET NUMBER:</b>      8      OF      20</p>	<p><b>TITLE:</b></p>
<p><b>DOCUMENT NUMBER:</b>      105_d12900_00A</p>	
<p><b>NOTES:</b>      NOTE</p>	



Linear Regulators

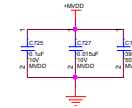
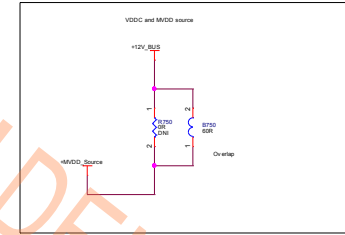


AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: 0.95V	REV: P00
DATE: Thu Jan 05 05:41:56 2017	
SHEET NUMBER: 11 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	

CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC. © 2014 Advanced Micro Devices The AMD Board schematic and design is the exclusive property of AMD, and is provided only to the customer under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a signed Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding the accuracy and design, including, but not limited to, any product warranty, or non-infringement or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information contained herein.	
TITLE: XXX	

Regulator for MVD0

Use +VDDC\_Source to share L601 to eliminate PCB's 12V current slew rate over spec (max) 1A/us  
+MVD0\_Source



AMD - PLATFORM HARDWARE ENG  
#46, No.1387, ZHANGDONG ROAD  
SHANGHAI, CHINA 201203



CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC.  
#14 Advanced Micro Devices  
This AMD Board schematic and design is the exclusive property of AMD, and  
it is intended for internal use only and is not to be distributed outside AMD.  
For evaluation purposes. Further distribution or disclosure is strictly  
prohibited. Use of this schematic and design for any purpose other than  
evaluation requires a signed Technology License Agreement with AMD.  
AMD makes no representation or warranty of any kind regarding the  
accuracy and design, including, but not limited to, any product safety  
or reliability, or those for a particular purpose, and disclaims  
responsibility for any consequences resulting from use of the  
information contained herein.

SHEET: MVD0

DATE: Wed Aug 29 03:24:48 2018

REV: P00

SHEET NUMBER: 12 OF 20

DOCUMENT NUMBER: 105\_d12900\_00A


TITLE:

XXX

Regulator for M100

Use rVDDC\_Sense to share L801 to eliminate PCIe 12V current slew rate over spec issue(0.1A/us)  
rVDDC\_Sense

AMD - PLATFORM HARDWARE ENG  
#46, No.1387, ZHANGDONG ROAD  
SHANGHAI, CHINA 201203



SHEET: VDDCI

DATE: Wed Aug 29 03:24:49 2018

SHEET NUMBER: 13 OF 20

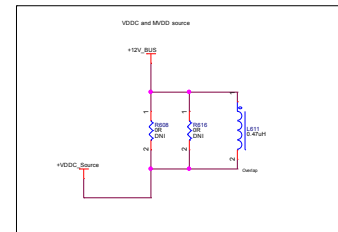
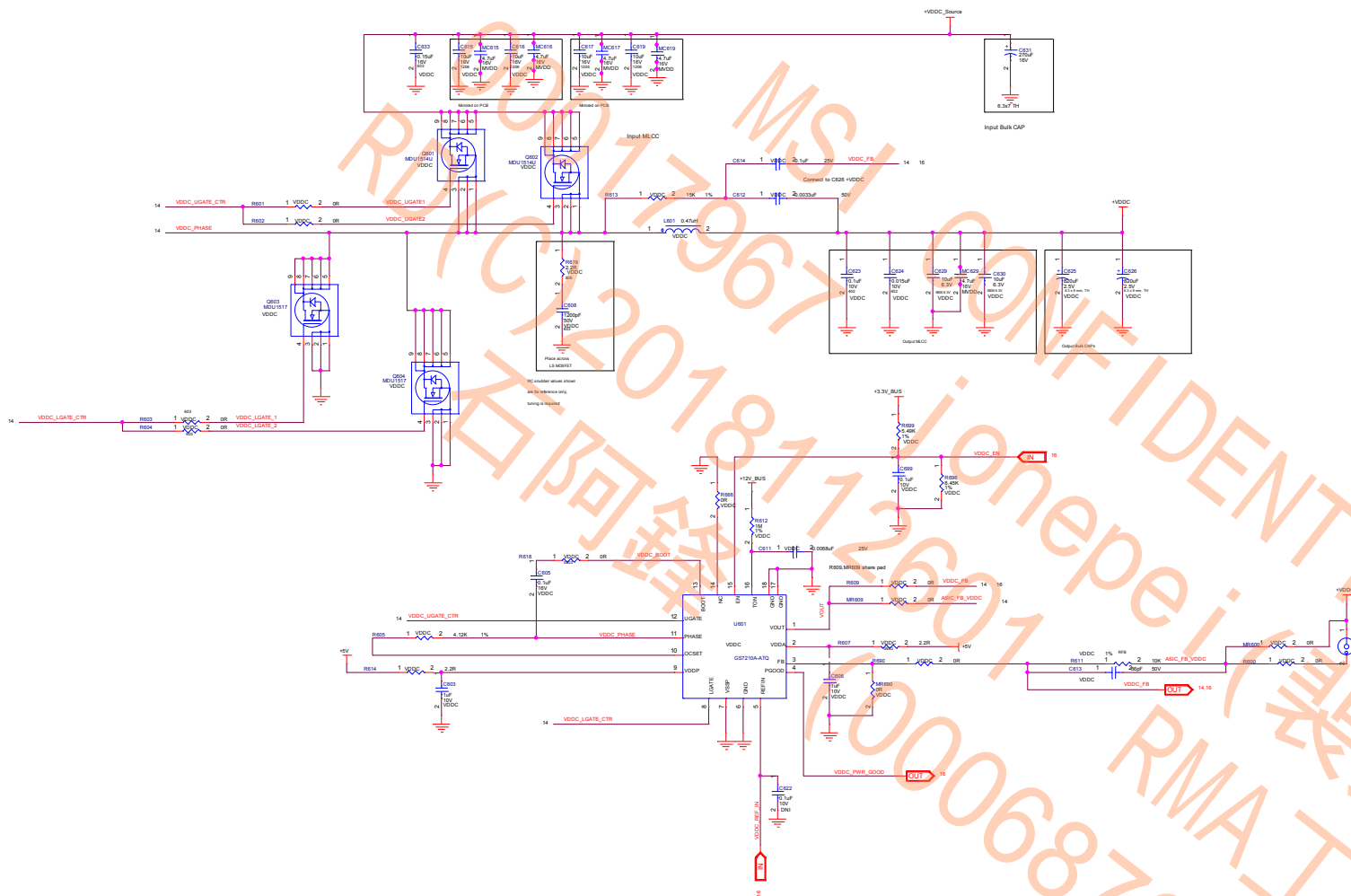
DOCUMENT NUMBER: 105\_012900\_00A


NOTES: NOTE

REV: P00

TITLE: XXX

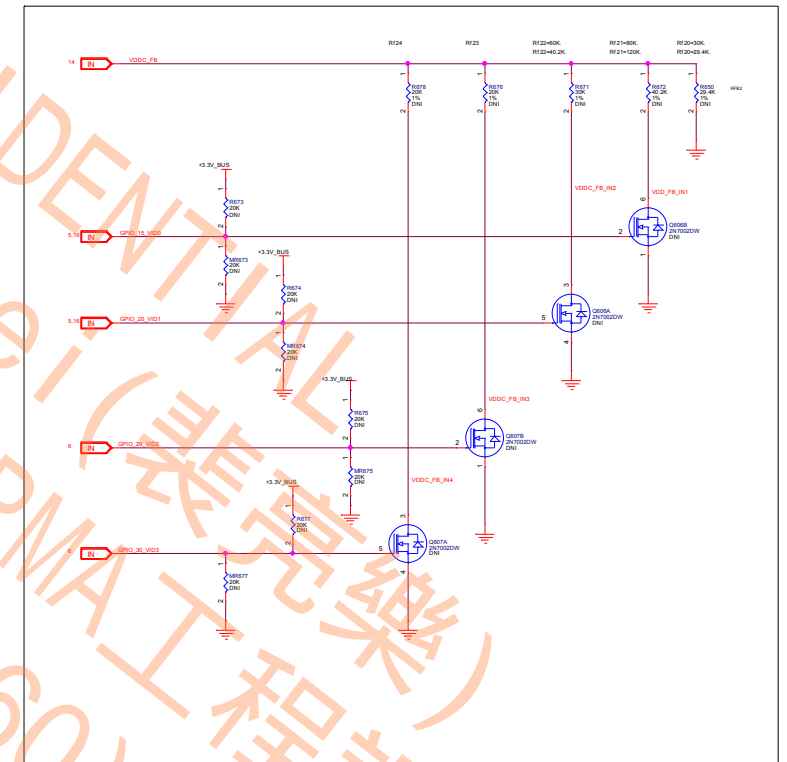
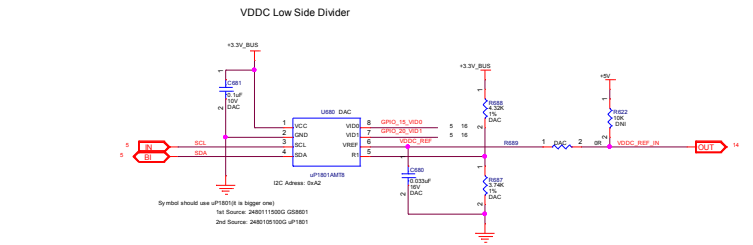
CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC.  
© 2018 Advanced Micro Devices  
This AMD Board schematic and design is the exclusive property of AMD, and is provided only under a non-disclosure Agreement with AMD.  
No third party reproduction, either electronic or mechanical, is allowed without the prior written permission of AMD.  
AMD makes no representations or warranties of any kind regarding the accuracy and design, including, but not limited to, any printed matter or electronic data, for any specific purpose, and disclaims responsibility for any consequences resulting from use of the information contained herein.



AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203				<small>CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC.</small> <small>© 2016 Advanced Micro Devices</small> The AMD Board schematic and design is the exclusive property of AMD, and is intended only for internal use and is not to be distributed, copied, or otherwise used for any purpose other than the specific purpose for which it was created. AMD makes no representation or warranty of any kind regarding the accuracy or design, including, but not limited to, any product safety or reliability, or for any other purpose, and disclaims any responsibility for any consequences resulting from use of the information contained herein.	
SHEET: VDDC		REV: P00		TITLE:  XXX	
DATE: Thu Jan 05 05:41:56 2017		SHEET NUMBER: 14 OF 20			
DOCUMENT NUMBER: 105_d12900_00A					



Don't support BACO
Support CTF (Internal)



SHEET: V00C1	
DATE: Wed Aug 29 03:24:49 2018	REV: P00
SHEET NUMBER: 16 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	
NOTES: NOTE	

CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.

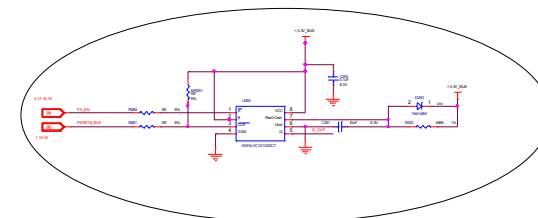
This AMD Brand schematic and design is the exclusive property of AMD, and is provided only for entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than that evaluation requires a Brand Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, but not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.

TITLE: \_\_\_\_\_

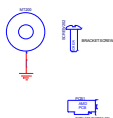
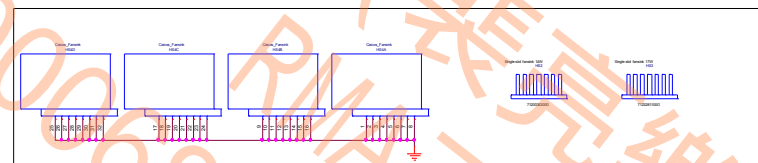
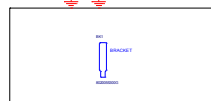
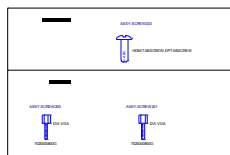
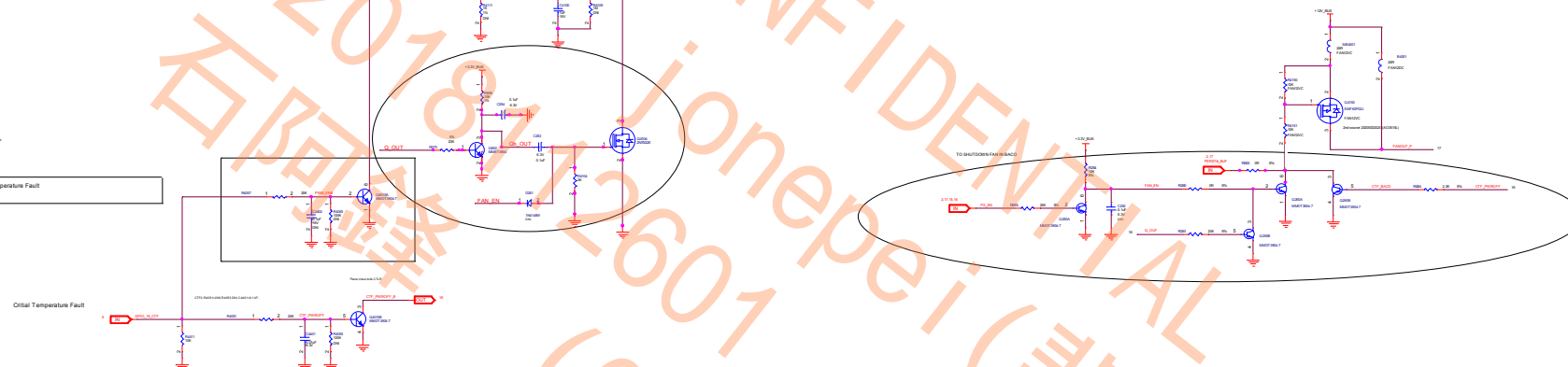
XXX



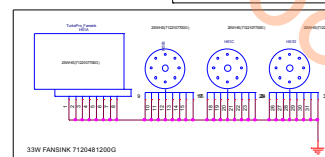
The above diagram illustrates a fan control system. A 24VDC power source is connected to a fan. The fan has two terminals labeled 'FANOUT\_A' and 'FANOUT\_B'. A control signal 'FAN\_CTRL' is connected to the 'FANOUT\_A' terminal. A ground symbol is connected to the 'FANOUT\_B' terminal. A box labeled 'Fan Control' is connected to the 'FAN\_CTRL' line.



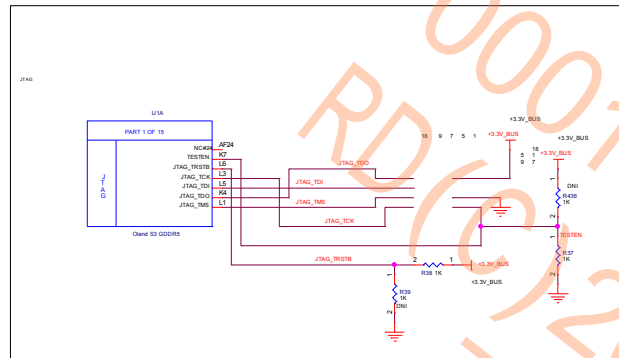
Critical Temperature Fault
----------------------------



Bracket, VGA, HDMI, DVI	8Q2005200G
Bracket, VGA, DP(16), DVI	8Q20052100G
Bracket, LP, DP(16), DVI	8Q20053300G
Bracket, LP, HDMI, DVI	8Q2005340G

[illegible]

FM1 SW\_FB  
FM2 SW\_FB  
FM3 SW\_FB  
FM4 SW\_FB  
FM5 SW\_FB  
FM6 SW\_FB

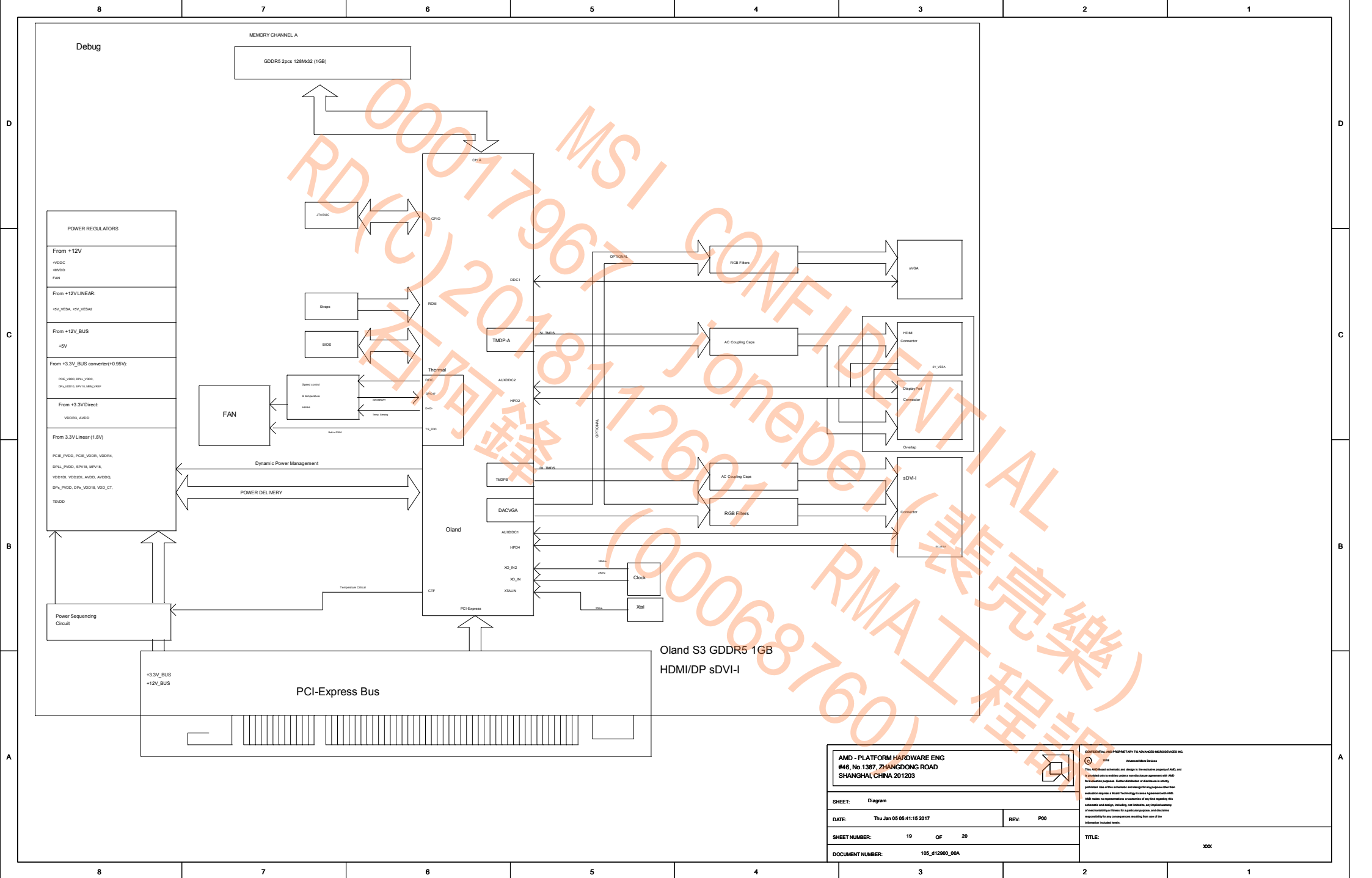


TOP  
Single end  
Memory data  
45 ohm +/- 5 ohm  
5.12 mils

Bottom  
Single end  
Address trunk  
45 ohm +/- 5 ohm  
5.12 mils


TOP  
Different  
TMS  
85 ohm +/- 10 %  
4.33 mils / 5.51 mils

Bottom  
Different  
PEX PCIE  
85 ohm +/- 10 %  
4.33 mils / 5.51 mils



	8	7	6	5	4	3	2	1																																																																																						
D	<table><tr><td colspan="3" rowspan="2"></td><td colspan="2">TITLE</td><td colspan="2">SCHEMATIC NO.</td><td colspan="2">DATE</td></tr><tr><td colspan="2">Olene D3 GD0R5 1GB HEMDP4DV1-VVGA</td><td colspan="2">105-012800-00A</td><td colspan="2"></td></tr><tr><td colspan="3" rowspan="2"></td><td colspan="5">REVISION HISTORY</td><td>NOTE: THIS SCHEMATIC REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC BOARD. FOR CORRELATED APPLICATIONS/PRODUCTS, PLEASE ALWAYS CONTACT THE PRODUCT SPECIFICATION. PLEASE CONTACT OUR REPRESENTATIVE TO OBTAIN LATEST INFORMATION TO THE APPLICATION BOARD.</td><td>REV</td></tr><tr><td colspan="5"></td><td>P00</td></tr><tr><td colspan="3">SCH REV</td><td colspan="3">PCB REV</td><td colspan="3">DATE</td><td colspan="2">REVISION DESCRIPTION</td></tr><tr><td colspan="3">10</td><td colspan="3">001</td><td colspan="3">2017-01-18</td><td colspan="2">Initial Board/PCB schematic based on Olen D3 GD0R5</td></tr><tr><td colspan="3"></td><td colspan="3"></td><td colspan="3"></td><td colspan="2"></td></tr><tr><td colspan="3"></td><td colspan="3"></td><td colspan="3"></td><td colspan="2"></td></tr><tr><td colspan="3"></td><td colspan="3"></td><td colspan="3"></td><td colspan="2"></td></tr></table>											TITLE		SCHEMATIC NO.		DATE		Olene D3 GD0R5 1GB HEMDP4DV1-VVGA		105-012800-00A							REVISION HISTORY					NOTE: THIS SCHEMATIC REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC BOARD. FOR CORRELATED APPLICATIONS/PRODUCTS, PLEASE ALWAYS CONTACT THE PRODUCT SPECIFICATION. PLEASE CONTACT OUR REPRESENTATIVE TO OBTAIN LATEST INFORMATION TO THE APPLICATION BOARD.	REV						P00	SCH REV			PCB REV			DATE			REVISION DESCRIPTION		10			001			2017-01-18			Initial Board/PCB schematic based on Olen D3 GD0R5																																		
			TITLE		SCHEMATIC NO.		DATE																																																																																							
			Olene D3 GD0R5 1GB HEMDP4DV1-VVGA		105-012800-00A																																																																																									
			REVISION HISTORY					NOTE: THIS SCHEMATIC REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC BOARD. FOR CORRELATED APPLICATIONS/PRODUCTS, PLEASE ALWAYS CONTACT THE PRODUCT SPECIFICATION. PLEASE CONTACT OUR REPRESENTATIVE TO OBTAIN LATEST INFORMATION TO THE APPLICATION BOARD.	REV																																																																																					
								P00																																																																																						
SCH REV			PCB REV			DATE			REVISION DESCRIPTION																																																																																					
10			001			2017-01-18			Initial Board/PCB schematic based on Olen D3 GD0R5																																																																																					
C																																																																																														
B																																																																																														
A																																																																																														

AMD - PLATFORM HARDWARE ENG  
#46, No.1387, ZHANGDONG ROAD  
SHANGHAI, CHINA 201203



SHEET:History

DATE:Thu Jan 05 05:41:18 2017


SHEET NUMBER:20 OF 20

DOCUMENT NUMBER:105\_012800\_00A

REV:P00

TITLE:  
XXX

CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC.

 0114 Advanced Micro Devices

The AMD Board schematics and design is the exclusive property of AMD, and is intended for internal use only. It is not to be distributed, copied, or otherwise used for any purpose other than evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a signed Technology Control Agreement with AMD. AMD makes no representation or warranty of any kind regarding the accuracy and design, including, but not limited to, any product safety or reliability, or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information contained herein.